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IN THE CLAIMS:

Please amend Claims 5, 8, 14, 15, 26, 28, 34, 38, 44, and 48 as follows:

5. (Amended) A sound source apparatus according to claim 4, wherein the adjusting device comprises a modifying device that modifies the algorithm to eliminate a predetermined one or more of the operation blocks involved in the system so as to reduce a number of the operation blocks to be loaded into the channel for adjustment to the condition.

8. (Amended) A sound source apparatus according to claim 4, wherein the adjusting device operates when the condition indicates that at least one of the operation blocks declines to become inactive in the system without substantially affecting other operation blocks of the system for eliminating said at least one operation block so as to reduce the number of the operation blocks to be allocated to the channel.

14. Please amend Claim 14, on page 102, as follows:

On line 6, please delete "adresses" and insert therefor --addresses--.

15. Please amend Claim 15, on page 102, as follows:

On line 19, please delete "mucical" and insert therefor -- musical --; and on line 23, please delete "mucical" and insert therefor -- musical--.

26. Please amend Claim 26, on page 106, as follows:

On line 20, please delete "adresses" and insert therefor --addresses--.

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28. Please amend Claim 28, on page 108, as follows:

On line 15, please delete "mucical" and insert therefor -- musical --;
on line 17, please delete "adresses" and insert therefor -- addresses --;
on line 20, please delete "mucical" and insert therefor -- musical --; and
on line 21, please delete "adresses" and insert therefor -- addresses --.

38.

34. (Amended) A method according to claim 31, wherein the step of adjusting comprises eliminating at least one submodule so as to reduce the number of the submodules to be loaded into the channel when the condition indicates that said at least one submodule loses contribution to computation of the waveform without substantially affecting other submodules.

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38. Please amend Claim 38, on page 112, as follows:

On line 15, please delete "mucical" and insert therefor -- musical --;
on line 17, please delete "adresses" and insert therefor -- addresses --;
on line 20, please delete "mucical" and insert therefor -- musical --; and
on line 21, please delete "adresses" and insert therefor -- addresses --.

48. ^{44.} (Amended) A machine readable media according to claim ~~41~~⁴⁵, wherein the step of adjusting comprises eliminating at least one submodule so as to reduce the number of the submodules to be loaded into the channel when the condition indicates that said at least one submodule loses contribution to computation of the waveform without substantially affecting other submodules.

48. Please amend Claim 48 as follows:

On page 116, line 25, please delete "mucical" and insert therefor -- musical --;

on page 117, line 1, please delete "adresses" and insert therefor -- addresses --;

on page 117, line 4, please delete "mucical" and insert therefor -- musical - -; and

on page 117, line 5, please delete "adresses" and insert therefor -- addresses --.

Please add new Claims 49-52 as follows:

12. ^{-40.} A sound source apparatus according to claim 1, wherein the generating device comprises a computing device responsive to a variable sampling frequency for executing the operation blocks to successively compute samples of the waveform in synchronization to the variable sampling frequency so as to generate the musical tone, and a controlling device for adjusting the variable sampling frequency dependently on a load of computation during the course of generating the musical tone.--

18.

--50. A sound source apparatus according to claim ~~18~~, wherein the processor device includes a delay device having a memory for imparting a delay to the waveform to determine a pitch of the musical tone according to the performance information, the delay device generating a write pointer for successively writing the samples into addresses of the memory and a read pointer for successively reading the samples from addresses of the memory to thereby create the delay corresponding to an address gap between the write pointer and the read pointer, the delay device being responsive to the fast sampling frequency to increment both of the write pointer and the read pointer by one address for one sample, otherwise the delay device being responsive to the slow sampling frequency to increment the write pointer by one address n times for one sample.--

19.

--51. A sound source apparatus according to claim ~~18~~, wherein the processor device includes a delay device for imparting a delay to the waveform to determine a pitch of the musical tone according to the performance information, the delay device successively writing the samples of the waveform of one musical tone into addresses of one memory region of the delay device and successively reading the samples from addresses of said one memory region to thereby create the delay, the delay device being operative when said one musical tone is switched to another musical tone for successively writing the samples of the waveform of said another musical tone into addresses of another memory region of the delay device and successively reading the samples from addresses of said another memory region to thereby create the delay while clearing said one memory region to prepare for a further musical tone.--

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As Filed

29. *28*
--52. A sound source apparatus according to claim *25*, wherein the processor means includes delay means having a memory for imparting a delay to the waveform to determine a pitch of the musical tone according to the performance information, the delay means generating a write pointer for successively writing the samples into addresses of the memory and a read pointer for successively reading the samples from addresses of the memory to thereby create the delay corresponding to an address interval between the write pointer and the read pointer, the delay means being responsive to the fast sampling frequency to increment both of the write pointer and the read pointer by every one address for every one sample, otherwise the delay means being responsive to the slow sampling frequency to increment the write pointer by every one address at n times for repeatedly writing one sample into consecutive n addresses. --